

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A. 110

APPLICATION NO. 09/727188	CONT/PRIOR	CLASS 748 703	SUBCLASS 16	ART UNIT 2784 2125	EXAMINER PALADINO
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Clinton Ramsey

Method and apparatus for generating transaction-based stimulus for simulation of VLSI circuits using event coverage analysis

PTO-2040
12/99[illegible]

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